

Abstract

A conversion circuit is devised to convert burst signals referencing to a plurality of clocks, respectively. A burst signal referencing to a first clock is decomposed into a plurality of non-burst signals by a plurality of phase signal
5 generators and corresponding signal fetching units. The plurality of non-burst signals are converted to signals referencing a second clock by a plurality of converters and those signals are synthesized into an output signal by a synthesizer. Therefore, the burst signal referencing to the first clock can be converted to the burst signal referencing to the second clock.

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